What is claimed is:

1. An auxiliary store selecting circuit adequate to a computer system with multifunction, comprising:

a selective switch part for generating a selection signal for selecting one out of numerous auxiliary memories in response to a selective manipulation;

a selection maintaining part for maintaining the generated selection signal till a power-off even though there is the selective manipulation after the generation of the selection signal in the selective switch part, and then outputting it; and

a selective connection part for performing a connection between the selected auxiliary memory and a computer main board in response to the selection signal outputted from the selection maintaining part.

2. The circuit of claim 1, wherein said selective switch part is composed of selective switches based on a 2-circuit 3-key interlocking method by the number corresponding to the number of the auxiliary memories.

3. The circuit of claim 1, wherein said selection maintaining part includes at least a flipflop integrated circuit connected to said selective switch part.

4. The circuit of claim 1, wherein said selective connection part comprises a relay for performing an electric connection between specific pins of the auxiliary memory selected and specific pins of a computer main board, and a transistors for use of a driving, for driving said

relay.

5. The circuit of claim 4, wherein said specific pins includes a read pin and a write pin.

6. A method for selecting an auxiliary memory in a computer system on which a plural number of auxiliary memories are mounted to construct multifunction, comprising the steps of:

generating a selection signal for selecting one out of numerous auxiliary memories in response to a selective manipulation;

holding the generated selection signal till a power-off; and

performing an electric connection between the auxiliary memory to be selected and a computer main board in response to the selection signal held.

7. The method of claim 6, wherein said auxiliary memory is a hard disk drive based on an IDE system.

8. A hard disk drive selecting circuit adequate to a computer system having an installment of a plurality of hard disk drives, comprising:

a selection switching unit for generating a selection signal for selecting one out of the hard disk drives in response to a selective manipulation of a user;

a selection maintaining unit having a latch circuit thereinside, for continuously maintaining the selection signal of the selection switching unit during a power-on, and then outputting it; and

a selection connecting unit for performing an electric

connection between read and write pins of the hard disk drive selected and its corresponding pins of a computer main board in response to the selection signal outputted from the selection maintaining unit.

9. The circuit of claim 8, wherein said selection maintaining unit further comprises a light emitting diode to visually display and inform the outside of that a specific hard disk drive was selected in response to a selective manipulation.

10. An auxiliary store selecting circuit adequate to a computer system with multifunction, comprising:

a selective switch part for generating a selection signal for selecting one out of numerous auxiliary memories installed in the computer system in response to a selection;

a selection maintaining part for maintaining the generated selection signal till a power-off even though there is the selective manipulation after the generation of the selection signal in the selective switch part, and then outputting it; and

a selective connection part including a connecter unit for automatically performing a connection between the selected auxiliary memory and a computer main board in response to the selection signal outputted from the selection maintaining part.

11. The circuit of claim 10, wherein said selection maintaining part includes at least a flipflop integrated circuit connected to said selective switch part and operating at about 12V.

12. The circuit of claim 10, wherein said connecter unit contains

a switch block constructed with numerous connecters and numerous integrated circuit elements.